IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF:

Peyman Hadizad

FILED:

Concurrently Herewith

FOR:

VERTICAL COMPOUND SEMICONDUCTOR

FIELD EFFECT TRANSISTOR STRUCTURE

Date:

7/18/2003

DISCLOSURE STATEMENT UNDER 37 C.F.R. 1.56

Honorable Assistant Commissioner for Patents Washington, D.C. 20231

SIR:

It is respectfully requested that the prior art listed on FORM PTO-1449 be considered in the examination of the subject application and made of record therein. A copy of the listed prior art is enclosed herewith.

No representation is made or intended that the listed prior art enclosed herewith is material to patentability of the subject patent application.

No representation is made or intended that a search has been made or that no better prior art than that listed is available.

Respectfully submitted,

Peyman Hadizad

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Reg. #44,358

Phone: 602-244-4885 Fax: 602-244 3169

Customer #: 27255

Form PTO-1449 LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT (Use Several Sheets If Necessary) ATTY. DOCKET NO. SERIAL NO. Ons00501 APPLICANT Peyman Hadizad FILING DATE CONCURRENTLY

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U.S. PATENT DOCUMENTS

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1	AΑ	6	3	5	6	0	5	9	3/12/2002	Yu	G05F	1/613	
1	AΒ	5	6	1	0	0	8	5	3/11/1997	Yuan et al	H01L	21/265	
1	AC	5	2	3	1	0	3	7	7/27/1993	Yuan et al	H01L	21/44	
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FOREIGN PATENT DOCUMENTS

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OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

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EXAMINER

DATE CONSIDERED

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.